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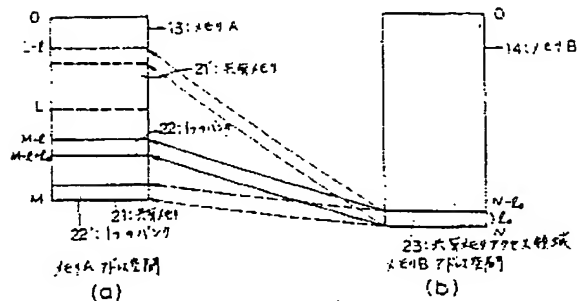
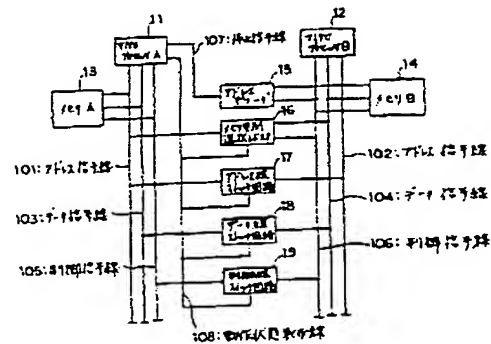
APPLICATION DATE : 12-12-86
APPLICATION NUMBER : 61294894

APPLICANT : OKI ELECTRIC IND CO LTD;

INVENTOR : SUDO NAGAKATSU;

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TITLE : SHARED MEMORY ACCESS SYSTEM



ABSTRACT : PURPOSE: To expand a dedicated memory area in each processor, by providing a shared memory only in one processor.

CONSTITUTION: When a microprocessor B12 performs data transfer to the shared memory 21, an address M-l is set on a memory spatial register 16 to select one bank 22 of the shared memory 21 from the microprocessor B12. Next, the microprocessor B12 accesses to an address N-l₀ in a shared memory access area 23. An address decoder 15 detects the fact that the shared memory access area 23 has been selected, and outputs a stop instruction to a microprocessor A11 automatically via a stop instruction line 107. The microprocessor A11 sends an operating state display signal which represents the stop of an operation to the memory spatial register 16, an address line switching circuit 17, a data line switching circuit 18, and a control line switching circuit 19 via an operating state display line 108.

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